

# SEAN DAVID SCOTT

# CURRICULUM VITAE

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London, England

## PROFILE

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- Eight years experience developing component and system level software and firmware specialising in low-level kernel and embedded software, error handling, diagnostic and test software.
- Work closely with hardware, QA, and cross-site teams to bring-up, debug, and test new products and processors.
- Lead by example with firm belief in the open sharing of work and ideas.
- Bring energy to a team and also effective working individually.
- Canadian citizen with the right to work and reside in the UK indefinitely.

## SUMMARY OF SKILLS

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<b>Languages</b>	8 years embedded/kernel/driver C and assembler 2+ years C++, TCL, and HTML
<b>Target Processors</b>	Sun UltraSPARC family, Motorola 68K family (CPU32), 68360, 68860/603/7410 (PowerPC), and HC16, Texas Instruments C5x and C25
<b>Target Systems</b>	Solaris, Linux, Nucleus, DOS, custom embedded OS's, VxWorks, and pSOS
<b>Basic System Admin</b>	Solaris, Linux, and OSX
<b>Test Equipment</b>	Oscilloscopes, logic analysers, In-Circuit Emulators, simulators, debuggers, and protocol analysers
<b>Technical Authoring</b>	Test and verification plans, interface specs, developer and user manuals, scope of work and project plans

## TECHNICAL EXPERIENCE

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### Senior Software Engineer, Platform OS

Sun Microsystems, Blackwater, Surrey, England  
www.sun.com/uk

Mar 2002-Aug 2004

- Planned and executed the UK software bring-up and validation of the Gemini dual core processor, including the lead role of determining and overseeing the required Solaris changes.
- Developed 'Error Injector' software/firmware to test the error handling and recovery of latest multi-core (CMT) UltraSPARC processors (Gemini and Niagara) as well as the associated Solaris error handling code.
- Developed Solaris and Hypervisor code in C and optimised assembler.

### Senior Software Engineer

Bravara Corp, Minneapolis, MN, USA  
www.bravara.com (defunct)

Jul 2001-Sep 2001

- Designed, planned, and implemented Power On Self Test (POST) and embedded runtime diagnostic firmware for a carrier class MPLS optical switch.
- Developed C and assembler code for a 7410 (PPC) and Galileo 64260 controller based design, involving the control of PCI buses, Ethernet devices, compact flash, hard disks (ATAPI), and I<sup>2</sup>C devices.

**TECHNICAL EXPERIENCE (con't)**

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**Senior Software Engineer**

**Computer Network Technology**, Plymouth, MN, USA  
www.cnt.com

**Nov 2000-Jul 2001**

- Developed T1/E1 driver for Ultranet Director product, a distributed embedded PPC based architecture running Nucleus Real-Time OS.
- Added features such as fragmentation and multi-channel support to LAPB, IP, and other network driver modules using Streams 5.1.

**Senior Diagnostics/Firmware Engineer**

**Digi International**, Minnetonka, MN, USA  
www.digi.com

**Nov 1999-Nov 2000**

- Took over and completed deliverable firmware for Sync T1/E1/PRI product line enabling it to ship on time after departure of lead software engineer.
- Designed a Linux based distributed qualification and production test and repair environment for the Sync product line using (also wrote the drivers and TCL application layer).
- Modified and expanded existing tests and code for design proving and production testing of Sync products (fixed/enhanced existing DOS/Windows code and ported key sections to Linux).
- Served as technical liaison to primary customer during alpha testing.

**Software Lead, Hardware Test Software**

**Computing Devices Canada (General Dynamics)**  
Calgary, AB, Canada  
www.gdcanada.com

**Jul 1996-Nov 1999**

- Lead 5 person software team to fully automate in-field testing of military communication equipment running on a ruggedised NT platform.
- Devised Fault Chain Analysis methods to isolate faults to a single FRU in the field, then down to component level once at repair facility.
- Developed test methods and software (embedded and host) in C/C++ and assembler for design proving, qualification, production, and operational testing of military communication hardware over numerous media and protocols, such as Ethernet, ISDN, Frame Relay, HDLC, and X.25.

**EDUCATION AND TRAINING**

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**B.Sc. Electrical Engineering, with Computer Engineering minor**  
University of Calgary, Calgary, AB, Canada

**1996**

**Sun Microsystems Training Courses**

Solaris System Administrator, Writing Device Drivers for Solaris,  
Solaris Internals, Solaris Crash Dump Analysis, and Sun Sigma.

**2002-2003**

**INTERESTS**

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- Electronic Music and Digital Audio Recording. Including multi-tracking, synthesis, sampling, filtering, and effects processing.
- Graphic design, electronic media and media rights.
- Snowboarding.
- Art house and French films.